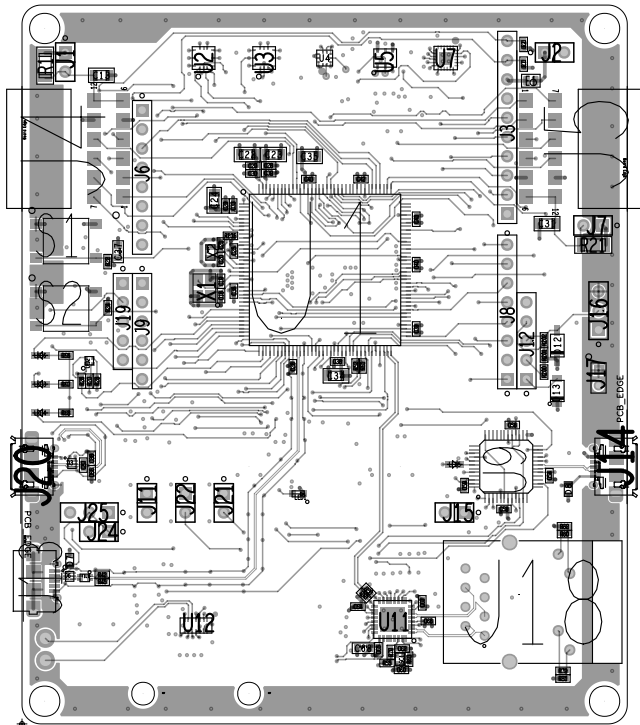


ART FILM - OUTLINE

A large, empty rectangular box with rounded corners, intended for an outline or drawing. The box is defined by a thin black border and occupies the central portion of the page.

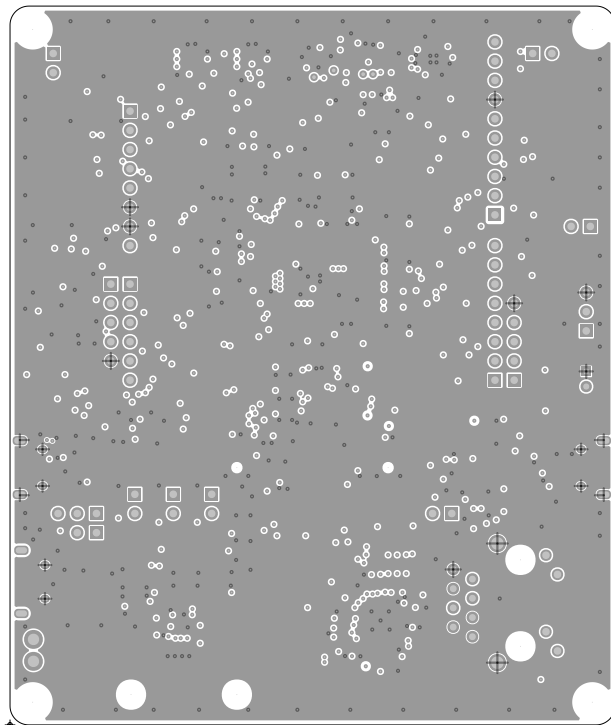
ART FILM - OUTLINE

ART FILM - TOP



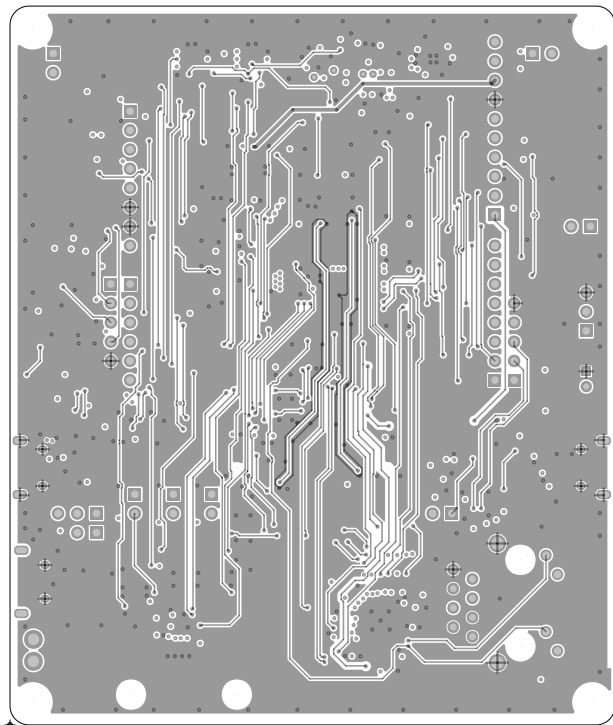
ART FILM - TOP

ART FILM - LAYER2



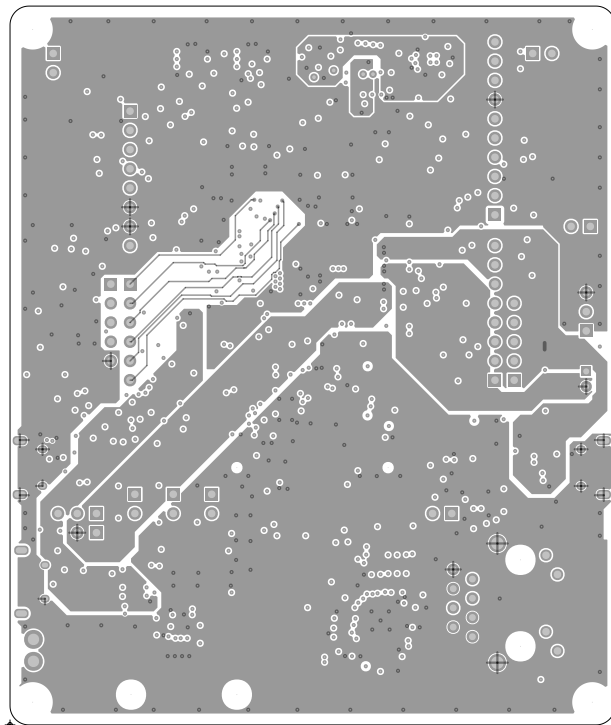
ART FILM - LAYER2

ART FILM - LAYER3



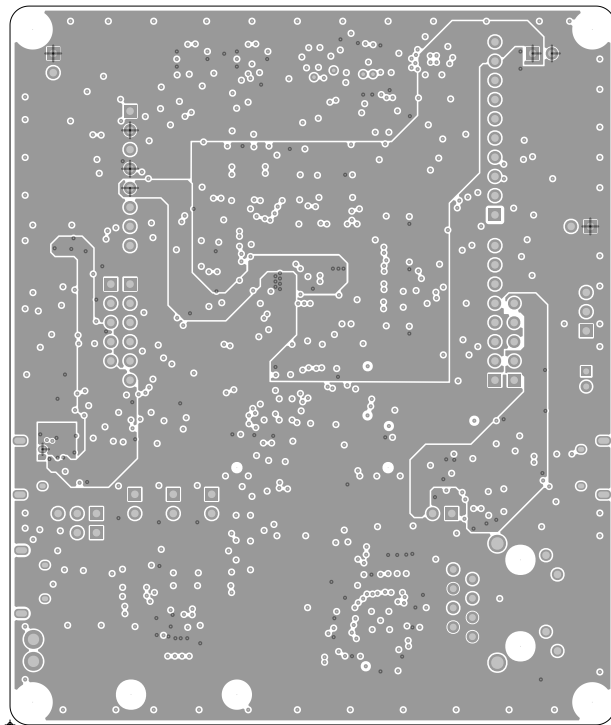
ART FILM - LAYER3

ART FILM - LAYER4



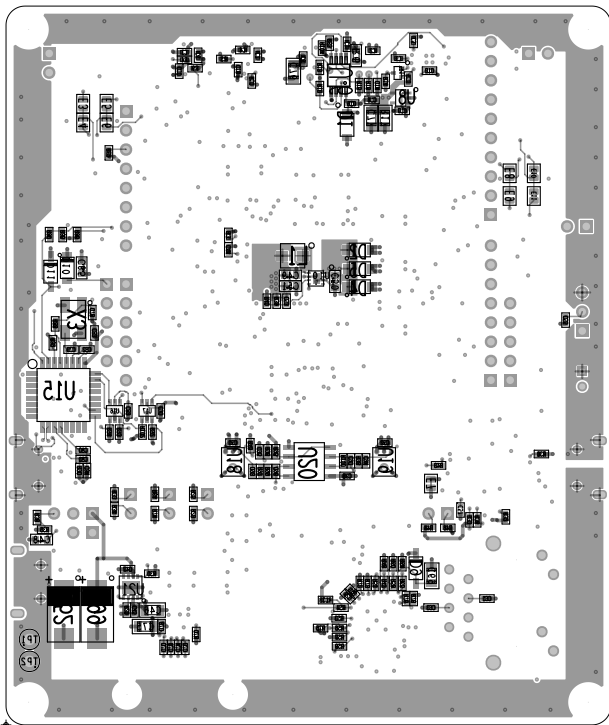
ART FILM - LAYER4

ART FILM - LAYER5

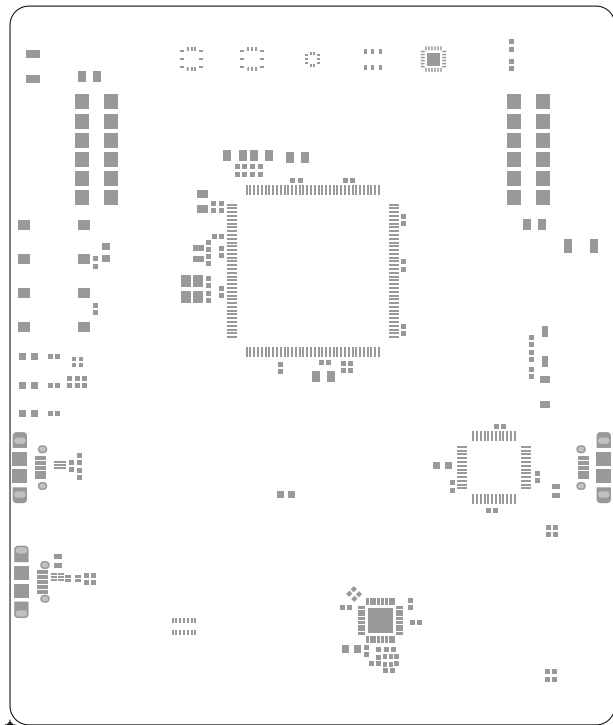


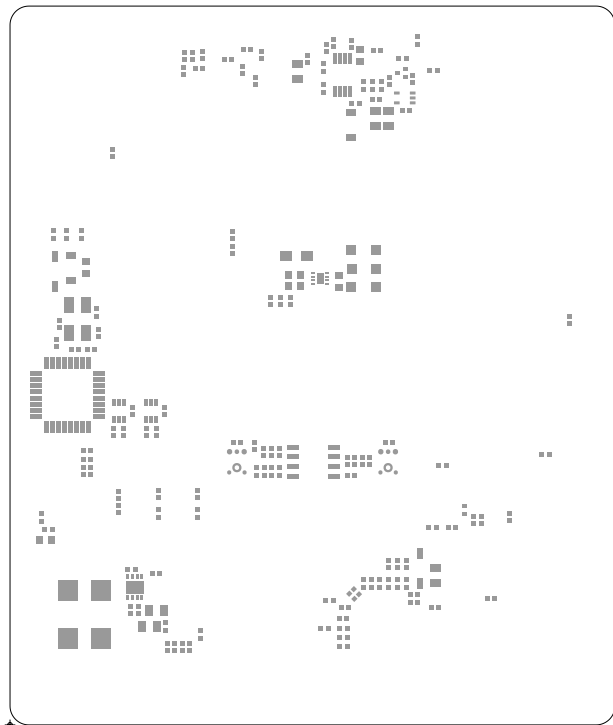
ART FILM - LAYER5

ART FILM - BOTTOM

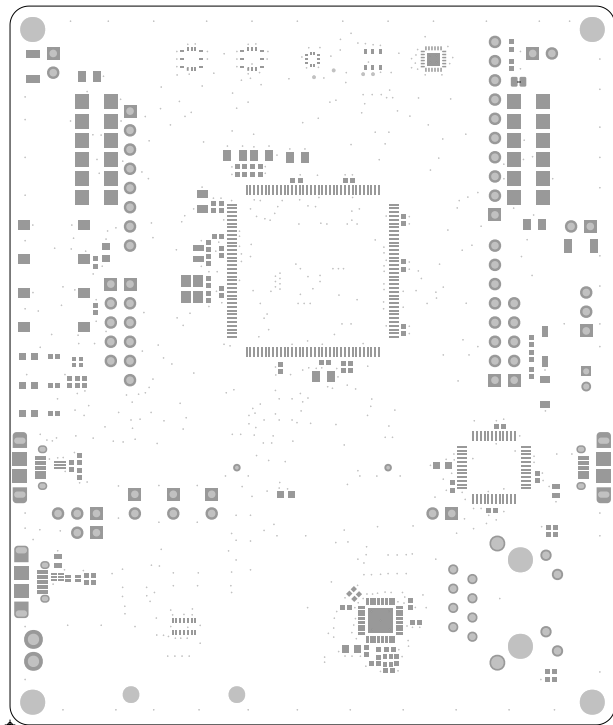


ART FILM - BOTTOM



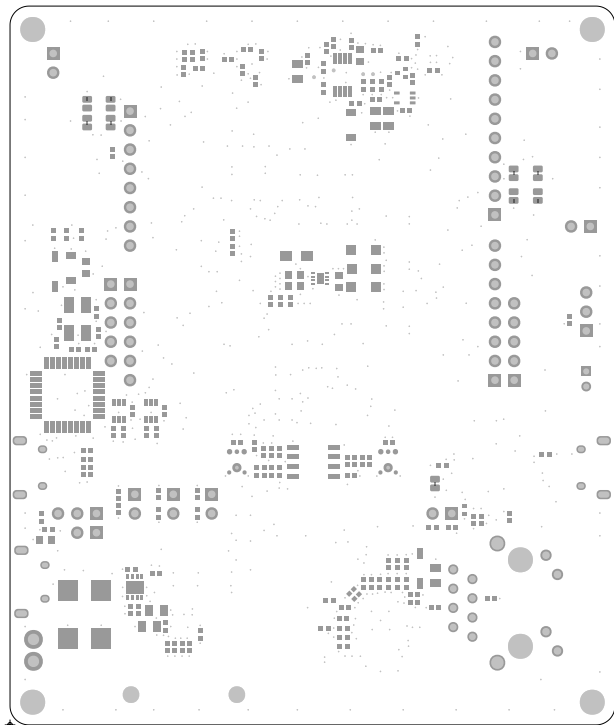


ART FILM - SOLDERMASK_TOP

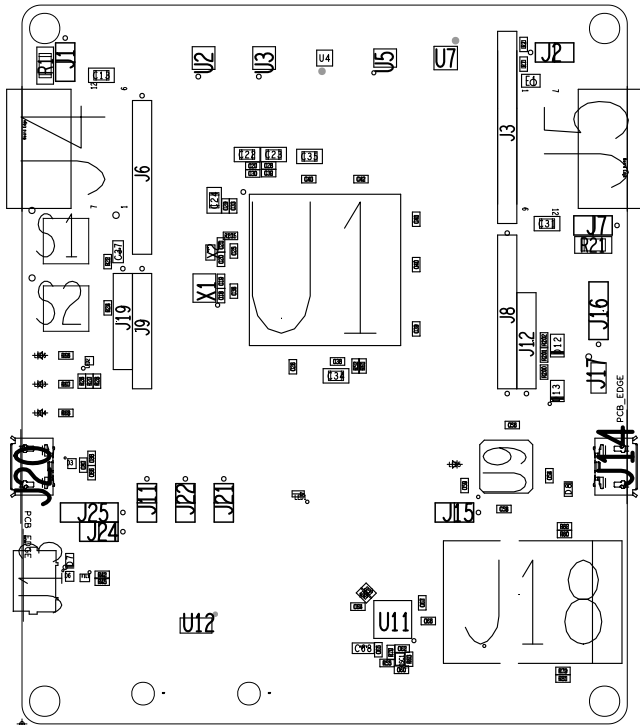


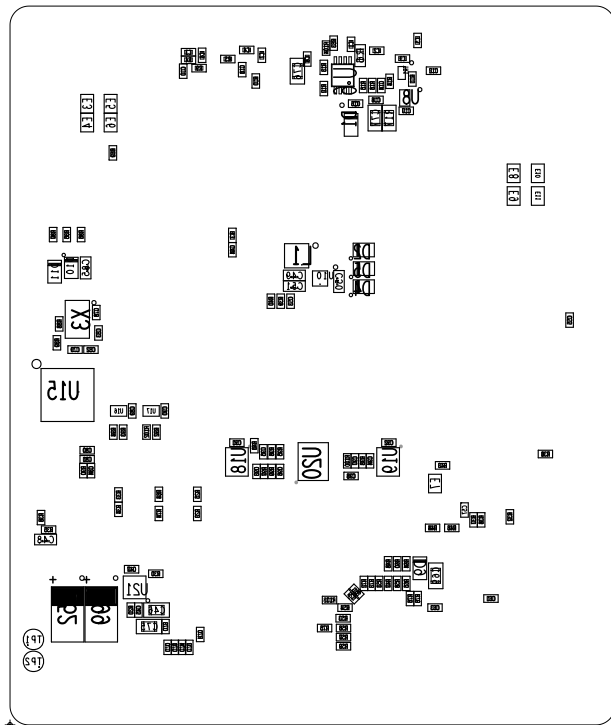
ART FILM - SOLDERMASK_TOP

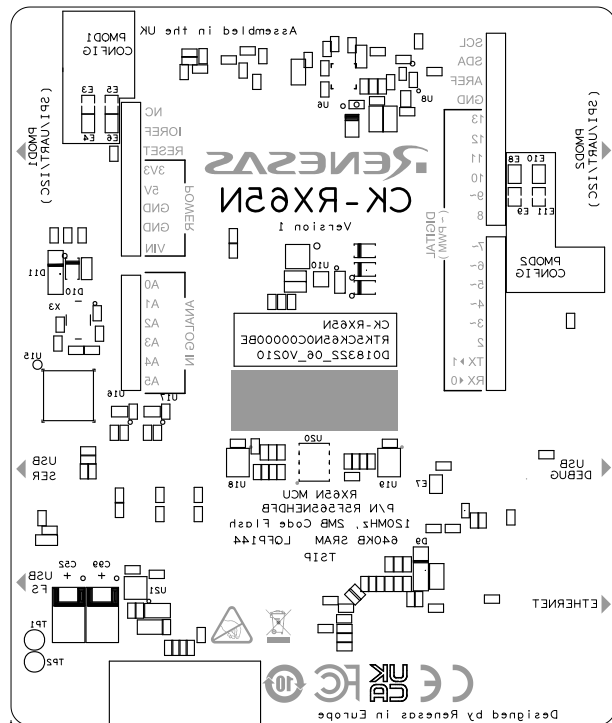
ART FILM - SOLDERMASK_BOTTOM



ART FILM - SOLDERMASK_BOTTOM

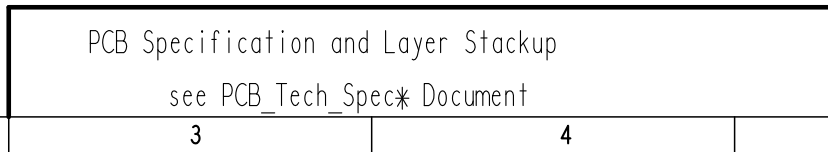






Alle Rechte ausdruecklich vorbehalten.
Vervielfaeltigung oder Mittheilung an Dritte,
gleichgueltig in welcher Form, ist ohne schriftliche
Genehmigung des Eigentumers nicht gestattet.

—

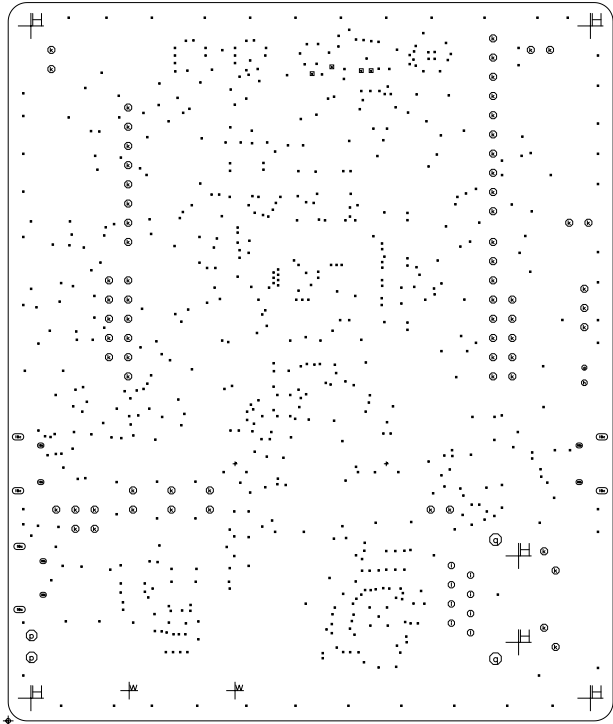


<div><div><div></div><div>Renesas</div><div>Semiconductor Solutions</div></div></div>	Title					Date	KM-Nr.	
	CK-RX65N							
	Code-Nr.							
	D018322_06_V0210							
Name	K.Hesse	Replacement for	These intangible goods are not subject to Annex I of common Dual-Use list (428/2009) in its current version.		SWV	Doc	Page	A3
Date	31.01.2022	-			V17.4	110	1 / X	
Copyright: Renesas Electronics Corporation. D-40472 Duesseldorf Germany								© 2022

All rights strictly reserved.
Reproduction or issue to third parties in
any form whatever is not permitted without
written authority from the proprietor.

CAD-ZEICHNUNG
manuelle Aenderung unzulassig !

Alle Rechte ausdrucklich vorbehalten.
Vervielfaeltigung oder Mitteilung an Dritte.
gleichzeitig in welcher Form, ist ohne schriftliche
Genehmigung des Eigentumers nicht gestattet.



DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILLIMETERS					
FIGURE	FINISHED SIZE	TOLERANCE_DRILL	TOLERANCE_TRAVEL	PLATED	QTY
•	0.2	+0.05/-0.05	-	PLATED	637
■	0.5	+0.1/-0.1	-	PLATED	4
●	0.8	+0.1/-0.1	-	PLATED	1
⊙	0.8	+0.1/-0.1	-	PLATED	1
⊖	0.9	+0.1/-0.1	-	PLATED	8
⊗	1.0	+0.1/-0.1	-	PLATED	68
⊕	1.5	+0.1/-0.1	-	PLATED	2
⊙	1.65	+0.1/-0.1	-	PLATED	2
+	0.5	+0.05/-0.05	-	NON-PLATED	2
⊕	2.2	+0.1/-0.1	-	NON-PLATED	2
⊕	3.3	+0.1/-0.1	-	NON-PLATED	6
●	0.85x0.65	+0.1/-0.1	+0.1/-0.1	PLATED	6
⊖	1.5x0.825	+0.1/-0.1	+0.1/-0.1	PLATED	6

TOTAL HOLES: 745

PCB Specification and Layer Stackup
see PCB_Tech_Spec* Document

Renesas	Title				CK-RX65N		Date	KM-Nr.		
	Code-Nr.				D018322_06_V0210					
Name	K.Hesse		Replacement for		These intangible goods are not subject to Annex I of common Dual-Use list (428/2009) in its current version.		SWV	Doc	Page	
Date	31.01.2022		-				V17.4	110	1 / X	A3
Copyright: Renesas Electronics Corporation, D-40472 Duesseldorf Germany									© 2022	

© 2022